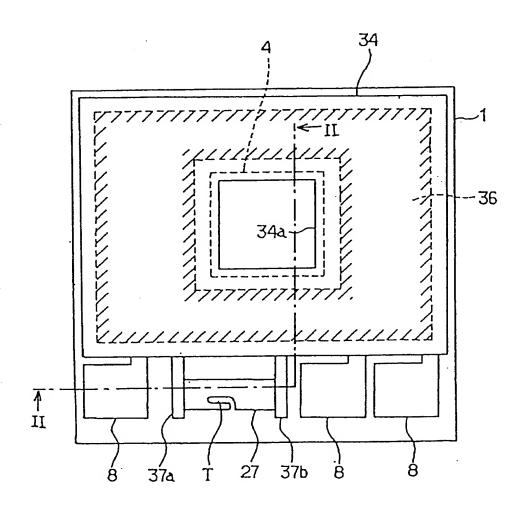
FIG. 1



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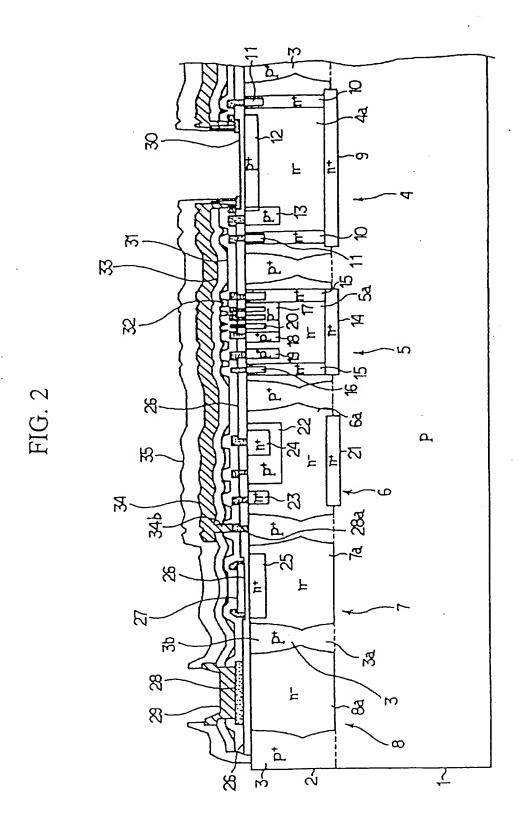
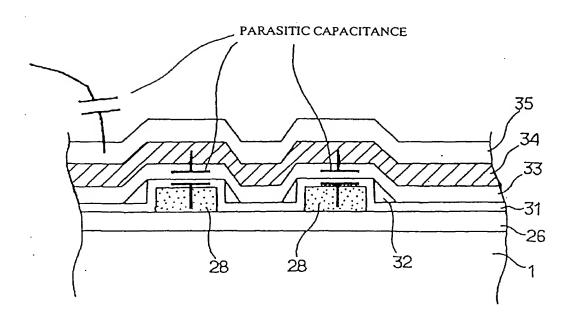


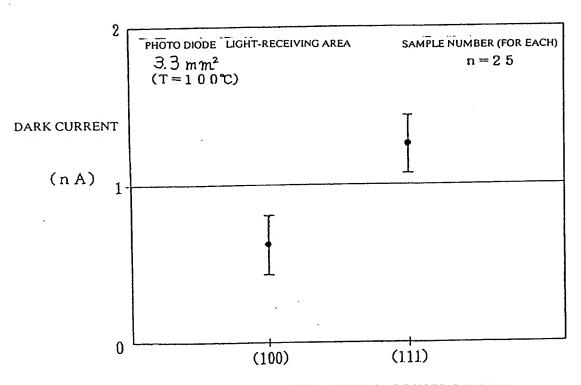
FIG. 3



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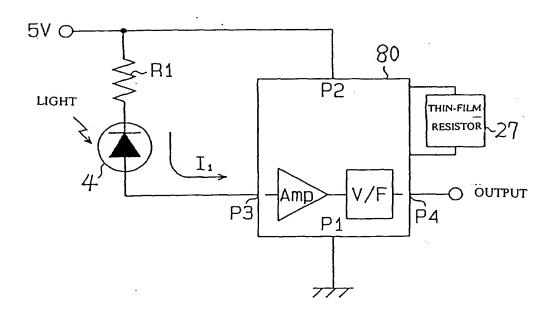
FIG. 4



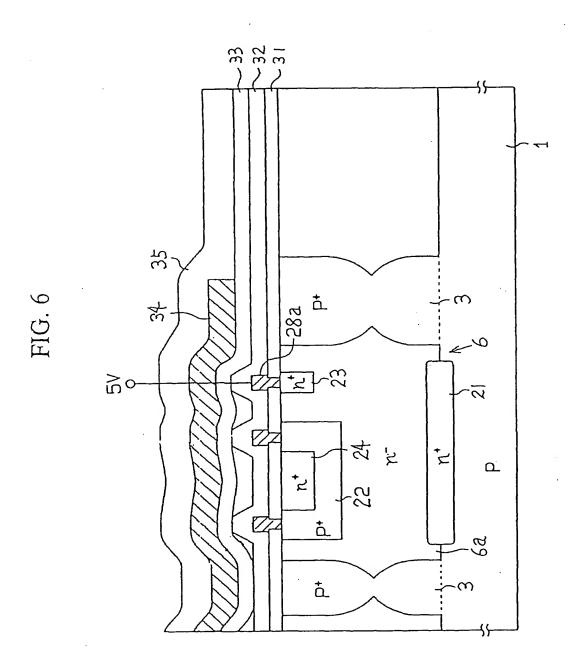
SURFACE AZIMUTH OF USED SUBSTRATE

Title: Photo Sensing Integrated Circuit Device And Related Circuit Adjustment Inventor: Inao Toyoda et al.
Atty. Ref. No.: 4041P-000005/DVA

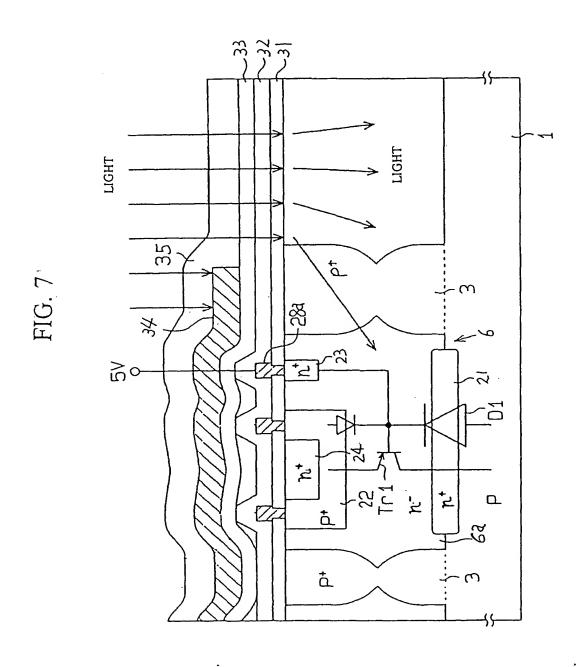
FIG. 5



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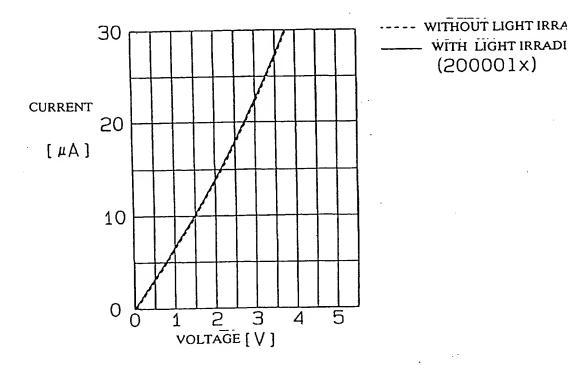
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FIG. 8



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FIG. 9

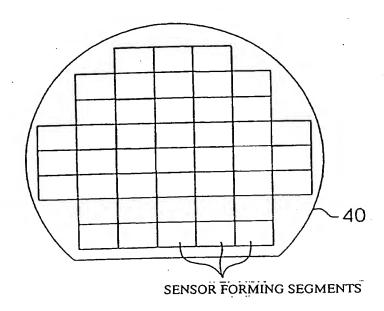
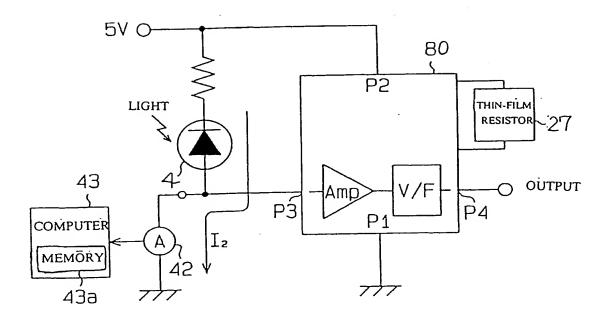
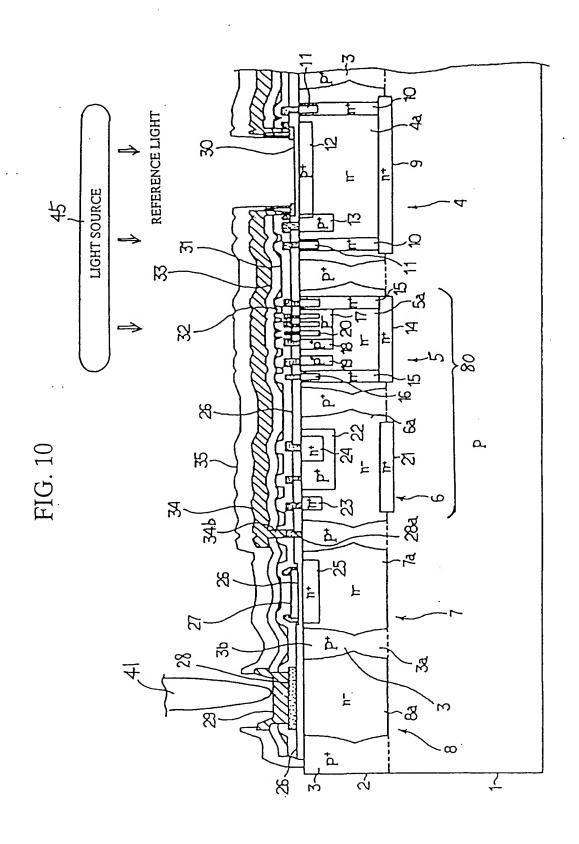


FIG. 11

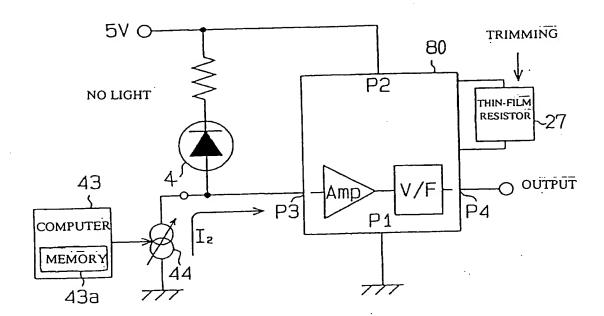


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FIG. 12



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